

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jung-Bae LEE and Kyu-Hyoun KIM

Serial No.: 09/875,364

Examiner: Tran, Khanh C.

Filed: June 5, 2001

Group Art Unit: 2611

Confirmation No.: 7447

For: SIGNAL TRANSMISSION CIRCUIT AND METHOD FOR EQUALIZING
DISPARATE DELAY TIMES DYNAMICALLY, AND DATA LATCH
CIRCUIT OF SEMICONDUCTOR DEVICE IMPLEMENTING THE
SAME

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

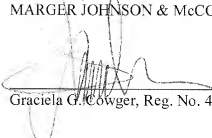
**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR
ALLOWANCE**

The Applicant notes that the Examiner has given some but not all of the reasons for allowing the claims. The Applicant believes that the prosecution record as a whole more clearly reflects the entirety of the reasons for allowing the claims over the cited prior art. The Applicant agrees that all the pending claims are allowable for the reasons expressed in earlier communications notwithstanding the Examiner's Statement of Reasons for Allowance.

The Applicant further asserts that the Examiner's Statement of Reasons for Allowance did not address many of the additional limitations included in, for example, the dependent claims. Those additional limitations render the corresponding claims novel and non-obvious over the cited prior art.

Customer No. 20575

Respectfully submitted,
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